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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTOR(S): Hooli et al.

SERIAL NO.: 10/783,049

ART UNIT: 2631

FILED: February 19, 2004

EXAMINER: Not Yet Assigned

TITLE: Chip-Level or Symbol-Level Equalizer Structure For Multiple
Transmit and Receiver Antenna Configurations

CONFIRMATION NO.: 6796

ATTORNEY DOCKET NO.: 872.0178.U1(US)

MAIL STOP AMENDMENT

Commissioner For Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

(37 C.F.R. §1.97(d))

Dear Sir:

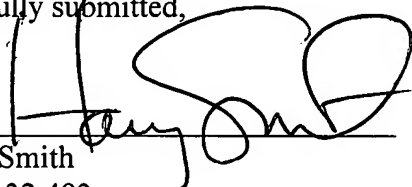
The following information is being disclosed to the U.S. Patent and Trademark Office as information that may be material to the examination of the above-identified patent application.

Applicants' Attorney is aware of the art listed on the enclosed Form PTO-1449. Copies of the non-U.S. patent art are enclosed with the Form PTO-1449 for the Examiner's use.

Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. Enclosed is a copy of the International Search Report mailed May 17, 2005 listing the cited references.

The filing of this Statement is not to be construed as a representation that a search has been made regarding the claimed invention (37 C.F.R. §1.97(g) or that no other possible material information exists. In addition, the filing of this Statement is not to be construed to be an admission that the information cited in the Statement is, or is considered to be, material to Patentability (37 C.F.R. §1.97(h)).

Respectfully submitted,



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CERTIFICATE OF MAILING

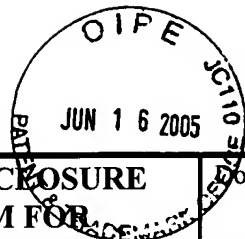
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

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U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
	US-2004/0127164 A1 US- US- US- US- US- US- US- US- US- US- US- US- US-	07-01-2004	Mondragon-Torres et al.	455	67.11

FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
	EP-1289182-A2 EP-1357693-A1 - - - - - - - -	03-05-2003 10-29-2003	Lucent Technologies Inc. Interuniversitair Microelektronica Centrum (IMEC)	

OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

	PETRE, F. et al., "Pilot-aided Adaptive Chip Equalizer Receiver for Interference Suppression in DS-CDMA Forward Link", IEEE: VTC 2000, pages 303-308,

Examiner's Signature:

Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
Include a copy of this citation form with your next correspondence to the Applicant(s).